



RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2154  
Docket No.: 1614.1210

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Teruhiko KAMIGATA, et al.

Serial No. 10/053,707

Group Art Unit: 2154

Confirmation No. 7916

Filed: January 24, 2002

Examiner: Larry D. Donaghue

For: INSTRUCTION PROCESSING METHOD FOR VERIFYING BASIC INSTRUCTION  
ARRANGEMENT IN VLIW INSTRUCTION FOR VARIABLE LENGTH VLIW  
PROCESSOR

**AMENDMENT AFTER FINAL REJECTION**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Attention: **BOX AF**

Sir:

This is in response to the Office Action mailed November 13, 2007, and having a period for response set to expire on February 13, 2008. A Petition for a 1-month extension of time, together with the requisite fee for same, is submitted herewith, thereby extending the period for response to March 13, 2008.

Reconsideration of the claims is respectfully requested. The following remarks are respectfully submitted.